

Abstract

The invention is directed to apparatus, methods, systems and computer program products which permit a simplification of the logic required for performing a certain function to a minimum set of logical elements of operations, permitting a complex digital circuitry to be simplified so that the processing speed for performing the complex digital operations, is reduced. This is accomplished by using a system of propositional logic (13B), representing the logic of a logical circuit to be designed as points and vectors in a vector space, simplifying the logic of the logical circuit to a simpler form using the points and vectors in a vector space, and designing the circuit using the simpler form.